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1 A VLSI priority packet queue with overwrite and inheritance

Picker, D.; Bendak, M.B.; Fellman, R.D.;

Computer Design: VLSI in Computers and Processors, 1994. ICCD '94. Proceedings., IEEE International Conference on , 10-12 Oct. 1994

Page(s): 551 -555

[\[Abstract\]](#) [\[PDF Full-Text \(496 KB\)\]](#) **IEEE CNF**

2 The ARTS real-time object model

Mercer, C.W.; Tokuda, H.;

Real-Time Systems Symposium, 1990. Proceedings., 11th , 5-7 Dec. 1990

Page(s): 2 -10

[\[Abstract\]](#) [\[PDF Full-Text \(744 KB\)\]](#) **IEEE CNF**

3 Conformance testing of priority inheritance protocols

Pollock, D.; Zobel, D.;

Real-Time Computing Systems and Applications, 2000. Proceedings. Seventh International Conference on , 12-14 Dec. 2000

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1 Priority inheritance protocols: an approach to real-time synchroniza
Sha, L.; Rajkumar, R.; Lehoczky, J.P.;

Computers, IEEE Transactions on , Volume: 39 , Issue: 9 , Sept. 1990

Pages:1175 - 1185

[\[Abstract\]](#) [\[PDF Full-Text \(1096 KB\)\]](#) **IEEE JNL**
2 Associative prioritized worker model with priority inheritance proto
Hong-Jin Park; Kyung-Ah Chun; Young-Chan Kim; Sung-Ik Jun;

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 1998 First International Symposium on , 20-22 April 1998

Pages:117 - 123

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3 Integrating real-time synchronization schemes into preemption threshold scheduling
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Pages:145 - 152

[\[Abstract\]](#) [\[PDF Full-Text \(398 KB\)\]](#) **IEEE CNF**
4 Conformance testing of priority inheritance protocols
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5 Experimental implementations of priority inheritance semaphore or ITRON-specification kernel

Takada, H.; Sakamura, K.;

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6 On using priority inheritance in real-time databases

Huang, J.; Stankovic, J.A.; Ramamritham, K.; Towsley, D.;

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8 A bandwidth inheritance algorithm for real-time task synchronization in open systems

Lamastra, G.; Lipari, G.; Abeni, L.;

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